UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant :

Yamazaki et al.

Art Unit:

2823

Serial No.:

10/024,850

Examiner:

Unknown

Filed

December 18, 2001

Title

SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING

THE SAME

Commissioner for Patents Washington, D.C. 20231

COPY OF PAPERS ORIGINALLY FILED

REQUEST TO CORRECT FILING DATE AND FOR CORRECTED OFFICIAL FILING RECEIPT

Applicant requests that the filing date of the above application be corrected from December 19, 2001, to December 18, 2001.

Enclosed is a copy of the return postcard, stamped December 19, 2001 by the PTO, and a copy of the Official Filing Receipt showing December 19, 2001 as the filing date. This date is in The correct filing date should be December 18, 2001, as shown by the enclosed copies of:

The application cover sheet bearing a signed certificate under 37 CFR §1.10, including the Express Mail

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I hereby certify under 37 CFR \$1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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receipt number EL584939116US, and a date of deposit of December 18, 2001;

- 2. The Express Mail receipt number EL584939116US, postmarked December 18, 2001; and
- 3. The Declaration of Mailing by "Express Mail" of Gabe Lewis, who delivered the application to the U.S. Post Office on December 18, 2001.

Please correct the filing date and supply a corrected

Filing Receipt to the undersigned with respect to this

application. A copy of the original Filing Receipt showing the

desired change in red ink is attached for your convenience.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 3/7/02

John O Phillips

Reg. №o. 35,322

Fish & Richardson P.C. PTO Customer No. 20985

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San Diego, California 92122

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APPLICATION

FOR

UNITED STATES LETTERS PATENT

TITLE:

SEMICONDUCTOR DEVICE AND METHOD OF

MANUFACTURING THE SAME

APPLICANT:

SHUNPEI YAMAZAKI, HISASHI OHTANI, JUN KOYAMA

AND TAKESHI FUKUNAGA

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December 18, 2001
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PATENT 07977/188007 ATTORNEY DOCKET NO.: 07977/188007
The Parent and Tracemark Office date stamp sets forth the receipt date (or both the receipt
Applicant: WAMACAK! ET AT. Title: Title: The Same
[\(\frac{1}{2} \)] Transmittal Letter [\(\frac{1}{2} \)] Applin \(\frac{1}{2} \)] Pages of Spec. \(\frac{5}{2} \) Pages of Claims \(\frac{18}{2} \) Total Claims \(\frac{1}{2} \) Pages of Abstract
[] Rule 62 Filing Request (FWC)Pages [] Assignment [] Deposit Account Order Form (2 Copies)
[Y] Check \$ 272— [] Small Entity Statement [Y] Drawings Sheets Formal Sheets Informal [X] Combined Declaration and Power of attorney signed unsigned
[] Preliminary Amendment—— Pages [K] Information Disclosure Statement [K] PTO 1449 Form—— Pages [] Prior Art References-Number of References—
[] Priority DocumentPages [] English Translation [K] OtherEXPRESS_MAIL_E_5849311605
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PATENT

Attorney's Docket No.: 07977-188002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Yamazaki et al.

Serial No.:

Filed:

Title

SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THE

SAME

Commissioner for Patents Washington, DC 20231

DECLARATION OF MAILING BY "EXPRESS MAIL"

Gabe Lewis declares as follows:

1. I reside at:

6333 College Grove Way, #A17, San Diego, CA 92115

and am an employee of:

EL584939116US

Fish & Richardson P.C. 4350 La Jolla Village Drive, Suite 500 San Diego, California 92122

- 2. On December 18, 2001, I deposited in the mail, "Express Mail Post Office to Addressee" service of the United States Postal Service, the contents of the envelope for which "Express Mail" receipt No. EL584939116US was issued addressed to the Commissioner for Patents, Washington, DC 20231
 - 3. Attached hereto is a true copy of the "Express Mail" receipt No. EL584939116US.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the above-identified application or any patents issued thereon.

Date: December 18, 2001

Hopiel Jeui





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APPLICATION NUMBER GRP ART UNIT FIL FEE REC'D ATTY DOCKET NO DRAWINGS IND CLAIMS FILING DATE TOT CLAIMS 07977-10/024,850 2823 992 14 18 6

188002/US3427/3434D

CONFIRMATION NO. 8129

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Date Mailed: 02/26/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Shunpei Yamazaki, Tokyo, JAPAN; Hisashi Ohtani, Kanagawa, JAPAN; Jun Koyama, Kanagawa, JAPAN; Takeshi Fukunaga, Kanagawa, JAPAN;

Assignment For Published Patent Application

Semiconductor Energy Laboratory Co., Ltd., a Japanese corporation;

Domestic Priority data as claimed by applicant

THIS APPLICATION IS A CON OF 08/951,819 10/14/1997

Foreign Applications

JAPAN 8-294419 10/15/1996 JAPAN 8-301250 10/24/1996

If Required, Foreign Filing License Granted 02/25/2002

Projected Publication Date: 06/06/2002

Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor device and method of manufacturing the same

Preliminary Class

257

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